

## Symposium V Chair



**Dr. Renhe Jia**  
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Dr. Renhe Jia is a Member of Technical Staff at Applied Materials Inc, Santa Clara, CA, currently working on the photovoltaic technologies and solar module reliabilities. He received a BS in mineral engineering from Anhui University of Science and Technology, 1993, and MS in 1999 and PhD in 2003 in Materials Science and Mineral Engineering from University of California, Berkeley.

Since 2002, Dr. Jia has been with Applied Materials Inc. working on advanced technology development in chemical mechanical planarization (CMP), electrochemical mechanical planarization(eCMP), cleaning, advanced packaging and lithography. Dr. Jia has been focusing on photovoltaic technology development and module reliability since 2007. Dr. Jia was manager in charge of the thin film module reliability group and currently working on advanced photovoltaic technology development.

Since 2004, Dr. Jia has been an industrial mentor for SRC/SEMATECH Engineering Research Center for Environmentally Benign Semiconductor Manufacturing. Dr. Jia served as a committee member/co-chair for CMP and Post-CMP Cleaning for International Semiconductor Technology Conference (ISTC) from 2006 to 2008, and for the symposium on Silicon Materials for Semiconductor and Photovoltaic Applications of the China Semiconductor Technology International Conference (CSTIC) from 2009 to 2011.

Dr. Jia has 13 published papers on surface/ interface chemistry, semiconductor and solar technology, and 26 granted/published patent applications in semiconductor/photovoltaic processing.