



**Yoichi Yano**

Executive Vice President and Member of the Board  
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Yoichi Yano received M.S. degree in Applied Mathematics from Kyoto University in Japan in 1980. He joined NEC Corporation in 1980, where he has lead the various development projects of Microprocessor and Microcontroller. He was in charge of chief architect of 32bit original microprocessors which was presented at ISSCC (International Solid-State Circuits Conference) in 1986. In 2002, He served as a General Manager of Microcomputer Business and Development at NEC Electronics Corporation, Vice President since 2004. Senior Vice President & Vice President since 2006. Executive Vice President since 2007. In 2008, he was named Executive Vice president and Member of the board, NEC Electronics Corporation. Since April 2010, he is Executive Vice president and Member of the board, Renesas Electronics Corporation.