

Symposium II Chair



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Dr. Henry F. Erk is a Senior Fellow in the Polysilicon and Quantitative Silicon Research Group in the Corporate Technology Department of MEMC Electronic Materials, Inc. He holds a D.Sc. degree in Chemical Engineering from Washington University in Saint Louis, Missouri. He has 31 years of experience in the manufacture of silicon wafers, including wafer slicing, lapping grinding etching and polishing; as well as experience in the manufacture of polysilicon raw material. He is co-inventor of 17 patents and has authored technical publications related to chemical reaction engineering, polysilicon production and silicon wafer manufacture. Current research activities include low-cost, high-quality silicon production routes, semiconductor and solar wafer etching, machining and polishing of semiconductor wafers and integrated manufacturing schemes to reduce cost.